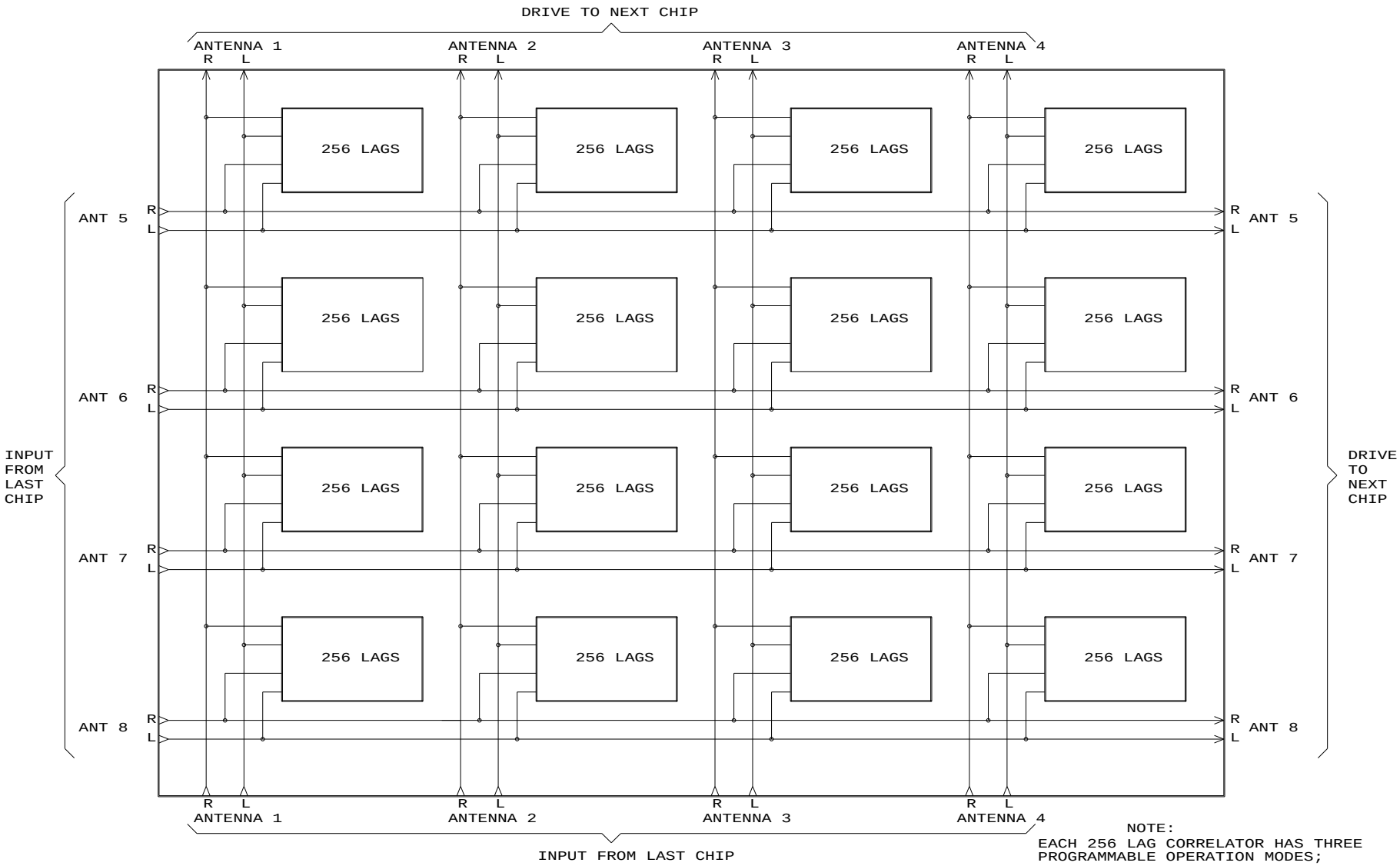
# FIGURE 4) CORRELATOR CHIP




NOTE:
EACH 256 LAG CORRELATOR HAS THREE PROGRAMMABLE OPERATION MODES;

1) FOUR 64 LAG CORRELATOR BLOCKS FOR RR, RL, LR, AND LL.
2) TWO 128 LAG BLOCKS FOR RR AND LL.
3) ONE 256 LAG BLOCK FOR EITHER RR OR LL.